

PRICE LIST

SIBEC-51A	8031 CPU, 11.0592 MHz clock; 8 k. Monitor-Debugger & Tiny BASIC. 8 k CMOS static RAM	\$335.00
SIBEC-51B	Same as above, but with 7.3728 MHz crystal	\$335.00
SIBEC-51C	Same as SIBEC-51B with the addition of a 2k by 8, 5v. only EEPROM - XICOR 2816	\$395.00
SIBEC-52A SIBEC-52B	These are the same as the 51-A and 51-B above with the substitution of the newer Intel 8032 CPU	\$375.00
SIBEC-52C	Same as 51-C but with the newer Intel 8032 CPU	\$435.00
SIBEC-52 BASIC	8052 Floating point BASIC with 8 k. RAM, prototyping area, iSBX expansion, PROM programmer, DMA	inquire
SIBEC-5XD	47 pg. documentation package with Tiny BASIC, sample program, schematics, etc.	\$12.50
SIBEC-5XL	Monitor Listing	\$45.00
SIBEC-51PS	"Wall Charger" type Power Supply. UL listed. 5 v. 500 ma. 12 v. 130 ma.	\$55.00
FORTH	by Bryte	\$100.00
XASM-51	Cross Assembler by AVOCET for both MS/DOS and CP/M. This creates an Intel hex file which is compatible with the SIBEC Monitor-Debugger "Down Load" capability CPM/80 CPM/86 or MSDOS	\$200.00 \$250.00

ORDERING INFORMATION

Orders may be placed by mail or phone.
Our mailing address is:

BINARY TECHNOLOGY INC.
MAIN ST.
MERIDEN, NH 03770

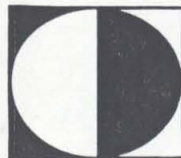
For telephone orders call:

603-469-3232

Binary Technology accepts VISA and MasterCard, personal or company checks, money orders, and will ship on open account with a purchase order number and credit approval. Please allow three weeks for processing of personal checks.

ADDITIONAL INFORMATION

Note that all of the boards listed on the Price List are identical except for the choice of crystal frequency, 8032 or 8032 CPU, and the additional EEPROM. The Monitor-Debugger and Tiny BASIC are provided in a 200 ns. ROM, so that the boards may be changed by simply substituting a different crystal. Also, the automatic Baud rate detect algorithm is independent of crystal frequency.



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SIBEC 51

SINGLE BOARD COMPUTER

CPU:	Intel 8031 (8032 optional)
Clock Rate:	11.0592 MHz or 7.3728 MHz
RAM:	8K · 8 CMOS static RAM
ROM:	8K monitor/debugger program and Tiny BASIC (or optional FORTH)
Non-volatile Memory:	Optional 2K, X2816 5v. only EEPROM
Parallel I/O:	20 pin connector with 2 8 bit I/O ports
Serial I/O:	1 RS-232 with auto baud rate detect
Expansion:	40 pin connector with all control, demultiplexed address and data (buffered) and a decoded external select line
Size:	5½" · 5½"
Power Req. (max):	5v at 300 ma (+12V & -12V at 25 ma if serial I/O used)
Memory Expansion:	4, 28 pin JEDEC locations
Address Decoding:	32 · 8 Fusible link PROM (socketed)
Jumpering:	All modern control lines, addressing, and memory device selection
Monitor Commands:	Clear or Set Bit, Disassemble, Trace, Dump Memory, Execute, Fill Memory, Go till Breakpoint, Dump Registers, Load Hex File, Substitute Memory, Modify Registers, Move Memory Block, Move Memory to EEPROM, Edit Command Line, BASIC
Tiny BASIC Commands:	CALL, FOR, HEX, LET, NEW, RETURN, ROM, TO, DECIMAL, GOTO, IF, LIST, PRINT, RAM, RUN, THEN, END, GOSUB, INPUT, NEXT, PROM, MONITOR, REM, RESTORE
BASIC Expressions:	ABS, DBYTE, OR, XBYTE, AND, MOD, RBIT, XOR, CBYTE, NOT, RND
BASIC Relational Operators:	, =, <, >, <=, >=, <>, <<, >>
Documentation:	47 page manual, data sheets, and programmers reference card
Accessories:	4 rubber feet and mating power connector provided

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