

Binary Technology, Inc. Main Street Meriden, NH 03770

PRICE SHEET

MICROCONTROLLER BOARDS

SIBEC-II Single-Board BASIC Controller **\$228**
Microcontroller board with Intel's 8052AH-BASIC V1.1 CPU with floating point BASIC interpreter. 11.0592 MHz clock. PROM programmer, two iSBX expansion connectors, prototyping area, 8K of RAM (expandable to 128K). RS-232 serial I/O. 5, 8-bit parallel I/O ports. Connectors with buffered address, data, and control lines. 7 3/4" x 5 7/16". Requires 5 VDC @ 500 ma. (Plus 12-25 VDC @ 50 ma. for PROM programming). Includes SIBEC-IID Documentation and SIBEC-B BASIC users manual.

SIBEC-II/8031 Single-Board Controller **\$206**
SIBEC-II as above less PROM programming hardware and equipped with 8031 CPU and SIBEC-F2 monitor/debugger.

SIBEC-IIPB Production Board **\$125 (Qty. 10-25)**

Same as SIBEC-II described above less PROM programming hardware, iSBX connectors, and reset button. Equipped with 8031 in place of the 8052AH-BASIC CPU. Other configurations available. Please inquire.

PDK51 Product Development Kit **\$595**
8031/8051/8052 product development kit. Includes everything required to go from an idea to working hardware. Requires only an IBM-PC/XT/AT or equivalent. Ideal for anyone involved in 8051 family software development. Includes SIBEC-II, SIBEC-F3, SXA-51 cross assembler, SIBEC-PS, SIBEC-PP, SIBEC-L, KERMIT, SIBEC-BS, full documentation, and a tutorial introduction based on a program for an interrupt-driven real-time clock. Also includes an RS-232 cable (AT users will need a DB-9 to DB-25 adaptor).

SOFTWARE and FIRMWARE

BXC51 BASIC Compiler **\$295**
8051/8052 BASIC compiler. For PC or MS-DOS. Produces 8051/31 machine code (in Intel hex format) from a BASIC-52 source. Fully compatible with code written for Intel's floating-point BASIC interpreter. Run BASIC-52 programs on an 8031 or 8032, and run them 2-50 times faster than before. Handles bit, byte, and integer extensions. Includes SXA51 cross assembler and complete documentation.

SXA51 Cross-Assembler **\$75**
8051/8052 cross-assembler for MS-DOS by Binary Technology, Inc. Creates an Intel Hex file compatible with the Binary Technology M/DP download capability, or any PROM programmer. With conditional assembly and full listing controls. Also included is a utility for merging and sorting multiple hex format files permitting modular program development (without requiring it). This results in a modify-assemble-test cycle that can be 2-5 times faster than when using an assembler that requires a linker.

SIBEC-F2 Monitor/Debugger **\$50**
Binary Technology, Inc.'s Monitor/Debugger Version 2.2. For assembly language development and debugging. Allows downloading of code from hosts such as IBM-PS, etc. into the SIBEC RAM. Your program may then be run under the control of the M/DP with trace, breakpoints, etc. Monitor commands include: Clear or Set bit, Disassemble, Trace, Dump memory, Execute, Fill memory, Go till breakpoint, Dump registers, Download Hex file, Kermit download hex file, Substitute memory, Modify registers, Move memory block, Move memory block to EEPROM, Edit command line. The M/DP version 2.2 is compatible with any SIBEC system.

SIBEC-F3 Monitor/Debugger **\$75**
M/DP Version 3.3 has all the features listed for Version 2.2 above, but is specifically for the SIBEC-II with the 8052AH-BASIC CPU. Most of the monitor resource utilization is moved into areas specific to the 8052, improving its usefulness for debugging code intended for the 8031/8051 family. In addition, the M/DP adds commands allowing the SIBEC-II to be used as a general purpose PROM programmer.

ACCESSORIES

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| SIBEC-PS Power Supply | \$45 |
| Wall charger type power supply with mating connector for SIBEC-II. UL listed, 5V at 500 ma. | |
| SIBEC-PP Programming Power Supply | \$65 |
| PROM programming power supply for SIBEC-II. Adjustable upto 26V at 250 ma. | |
| SIBEC-BX Cabinet | \$45 |
| Extruded aluminum cabinet for the SIBEC-II. One end panel is machined to accommodate the DB-25 connector and reset button. The other panel is blank to allow modifications by customer. | |
| SIBEC-IIBD Bare Board | \$45 |
| Bare printed circuit board for the SIBEC-II. | |
| SIBEC-C Clock | \$39 |
| Lithium battery backed real-time clock. Can be installed in any spare socket, or sandwiched between a ROM and its socket. Complete with assembly language routine for reading and setting times. | |

DOCUMENTATION

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| SIBEC-IISK Schematic | \$6 |
| SIBEC-II schematic. "D" size (24" x 36"), Specify Rev. 5 or Rev. 6. | |
| SIBEC-IID Hardware Documentation | \$14 |
| Hardware documentation for SIBEC-II. Includes SIBEC-IISK. | |
| SIBEC-IIB BASIC Manual | \$15 |
| Intel BASIC-52 Manual. | |
| SIBEC-L Source Listing | \$25 |
| Hardcopy listing of source for M/DP Monitor/Debugger. Specify V2.2 or V3.3. | |
| SIBEC-FD Monitor/Debugger Documentation | \$8 |
| Documentation for monitor/debugger (SIBEC-F2/SIBEC-F3). | |

HARDWARE and SOFTWARE SOURCES

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| SIBEC-IIBS BASIC Source | \$25 |
| Machine readable source for the Intel BASIC-52 version 1.1. Complete including all the floating point routines. (Put into the Public Domain by Intel). MS-DOS only. | |
| SIBEC-FS Monitor/Debugger Source | \$75 |
| Machine readable source code for M/DP. May be modified or otherwise used for in-house use by the purchaser. License prohibits any use that might compete with Binary Technology, Inc. in sales of general purpose and/or development hardware or software. | |

TERMS

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